



SAW Components

SAW filter

Short range devices

Series/type:	B3733
Ordering code:	B39431B3733H110
Date:	April 04, 2006
Version:	2.0

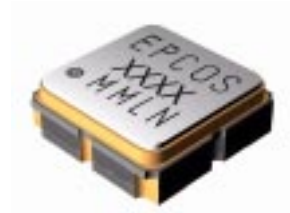


Preliminary data



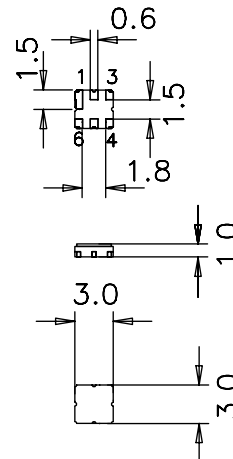
Application

- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible



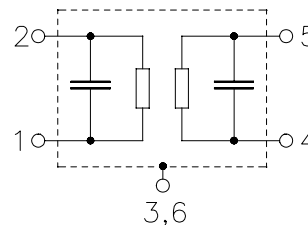
Features

- Package size 3.0 x 3.0 x 1.0 mm³
- Package code DCC6E
- RoHS compatible
- Approximate weight 0.037 g
- Package for **Surface Mount Technology (SMT)**
- Ni, gold-plated terminals
- Lead free soldering compatible with J - STD20C
- Passivation layer Elpas
- AEC-Q200 qualified component family
- **Electrostatic Sensitive Device (ESD)**



Pin configuration¹⁾

- 1 Input (recommended) or input ground
- 2 Input ground (recommended) or input
- 4 Output (recommended) or output ground
- 5 Output ground (recommended) or output
- 3,6 To be grounded



1) The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.



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434.42 MHz

Preliminary data



Characteristics

Operating temperature range: $T = -45\text{ °C to }+95\text{ °C}$
 Terminating source impedance: $Z_S = 50\ \Omega$ and matching network
 Terminating load impedance: $Z_L = 50\ \Omega$ and matching network

		min.	typ. @ 25 °C	max.	
Center frequency	f_C	—	434.42	—	MHz
Minimum insertion attenuation	α_{min}				
incl. loss in matching elements ($Q_L = 47$)		—	2.1	3.0	dB
excl. loss in matching elements		—	1.5	2.2	dB
Pass band (relative to α_{min})					
434.27 ... 434.57 MHz		—	0.6	2.0	dB
434.24 ... 434.60 MHz		—	0.8	3.0	dB
434.20 ... 434.64 MHz		—	1.2	6.0	dB
Relative attenuation (relative to α_{min})	α_{rel}				
10.00 ... 414.00 MHz		55	60	—	dB
414.00 ... 426.50 MHz		48	53	—	dB
426.50 ... 431.00 MHz		24	28	—	dB
431.00 ... 432.22 MHz		30	35	—	dB
432.22 ... 432.47 MHz		40	45	—	dB
432.47 ... 432.57 MHz		30	52	—	dB
432.57 ... 432.62 MHz		25	52	—	dB
432.62 ... 433.60 MHz		15	18	—	dB
435.42 ... 444.50 MHz		15	20	—	dB
444.50 ... 500.00 MHz		40	45	—	dB
500.00 ... 810.00 MHz		50	55	—	dB
810.00 ... 1500.00 MHz		60	65	—	dB
1500.00 ... 2500.00 MHz		53	58	—	dB
Impedance for pass band matching¹⁾					
Input: $Z_{IN} = R_{IN} \parallel C_{IN}$		—	350 \parallel 2.2	—	$\Omega \parallel$ pF
Output: $Z_{OUT} = R_{OUT} \parallel C_{OUT}$		—	350 \parallel 2.2	—	$\Omega \parallel$ pF

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.



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Maximum ratings

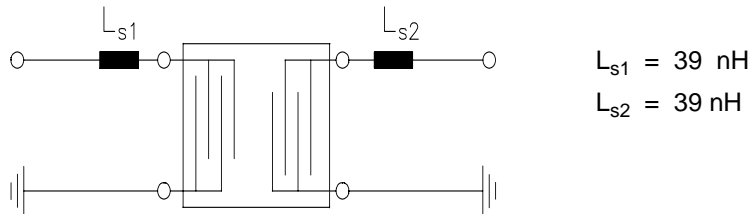
Operable temperature range	T	-45/+120	°C	
Storage temperature range	T _{stg}	-45/+120	°C	
DC voltage	V _{DC}	6	V	
Source power	P _S	10	dBm	source impedance 50 Ω



Preliminary data



Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



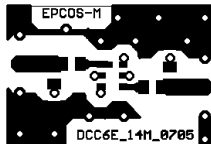
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the “ground-loop” problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers’ grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection



Optimised PCB layout for SAW filters in DCC6E package, pinning 1,4 (top side, scale 1:1)

The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

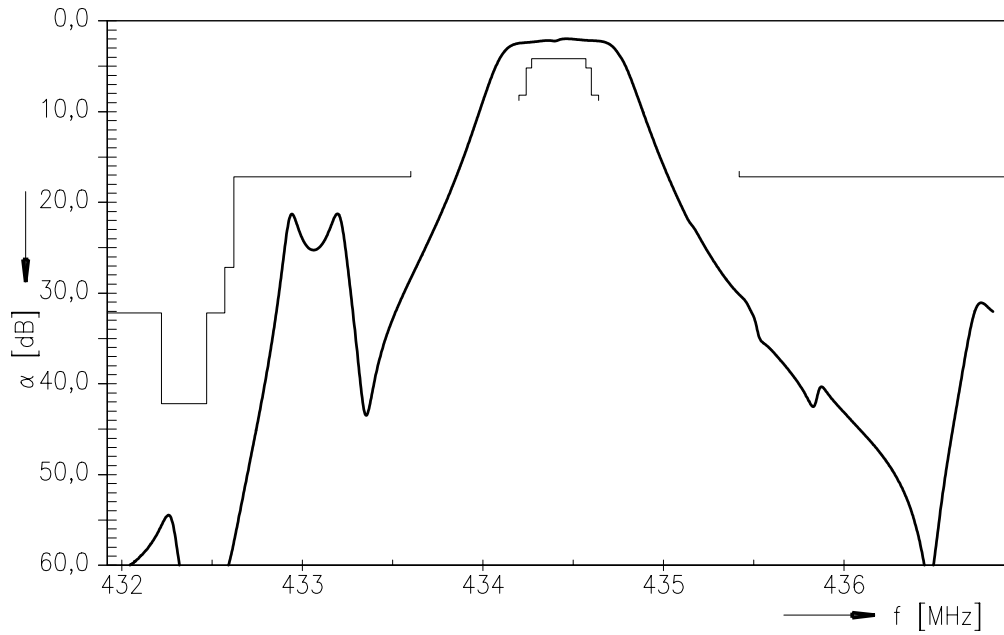
For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.



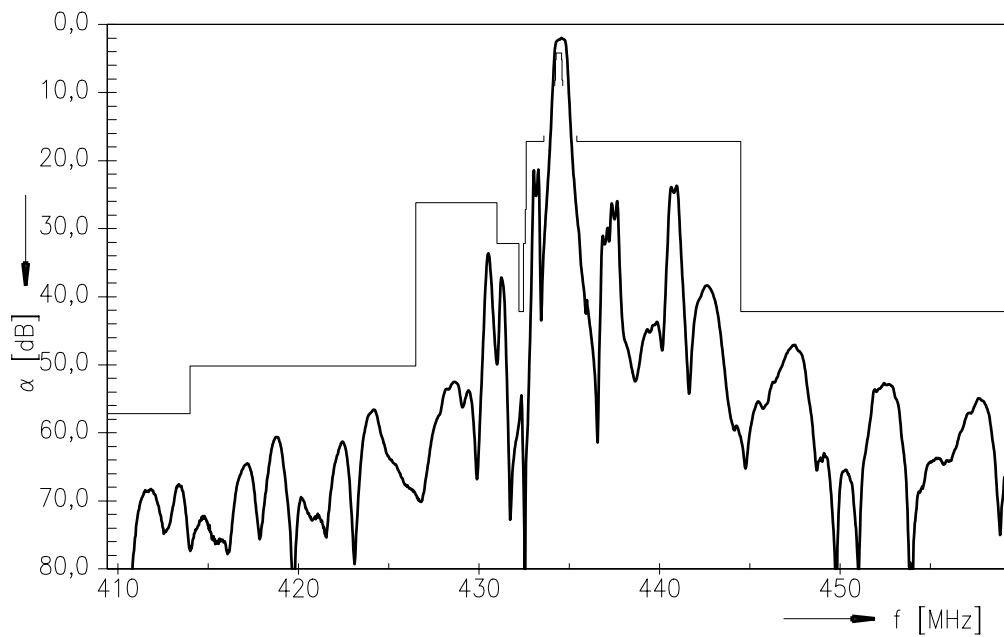
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Transfer function



Transfer function (wideband)





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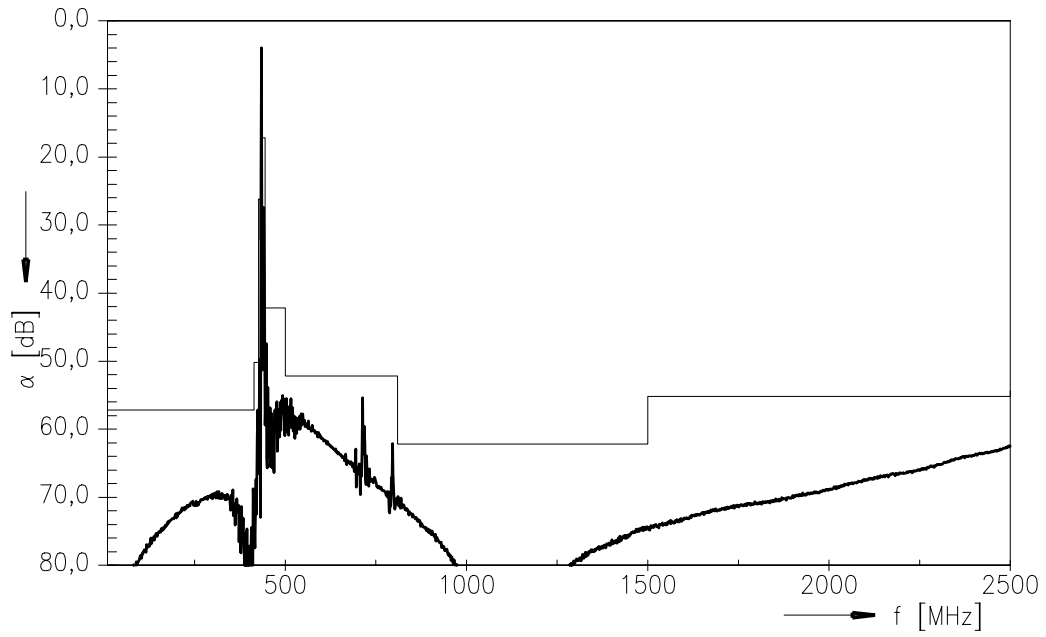
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Transfer function (ultimate rejection)



Please read *cautions and warnings and important notes* at the end of this document.



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References

Type	B3733
Ordering code	B39431B3733H110
Marking and package	C61157-A7-A143
Packaging	F61074-V8168-Z000
Date codes	L_1126
S-parameters	B3733_SB.s2p B3733_WP.s2p
Soldering profile	S_6001
RoHS compatible	defined as compatible with the following documents: "DIRECTIVE 2002/95/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment. 2005/618/EC from April 18th, 2005, amending Directive 2002/95/EC of the European Parliament and of the Council for the purposes of establishing the maximum concentration values for certain hazardous substances in electrical and electronic equipment."

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